



Simulated frequency crowding constraints for modular quantum architecture design Girgis Falstin¹, Evan McKinney², Michael Hatridge^{1,4}, Alex K. Jones^{2,3}

¹Department of Physics and Astronomy, University of Pittsburgh, Pittsburgh, PA 15213, USA ²Department of Electrical and Computer Engineering, University of Pittsburgh, Pittsburgh, PA 15213, USA ³Department of Electrical Engineering and Computer Science, Syracuse University, Syracuse, NY, 13244, USA ⁴Department of Applied Physics, Yale University, New Haven, CT 06511, USA



 $\lambda = .1$

06

0.6

Objective: Use simulations to investigate quantum architecture design tradeoffs for (a) residual qubit couplings (spectators) and (b) frequency crowding requirements, for varying coupling strengths.





Parametric interactions determined by differences in resonant mode frequencies

— Resonant Qubit frequencies





Simulated gate crowding:





Frequency allocation for an ideal iSWAP



Conclusions:

 \succ Stronger hybridization (λ) makes faster gates but more noise from spectators \succ Increase in λ from .1 to .3 resulted in decreasing fidelity gap for $\lambda^2 \rightarrow \lambda^4$ from roughly 3 orders of magnitude to 2 Increased qubit density reduces SWAP overhead but with frequency crowding > As gates become increasingly far from pumped SNAIL, fidelity grows from ~.3 to >.99 for intramodular vs intermodular gates

Discrete frequency allocation:

Frequency packing grows factorially with # qubits

When is frequency allocation feasible?



[1] Zhou, et al. npj Quantum Inf. (2023) [2] Ding, et al **MICRO** (2020) [3] Zajac, et al. *arXiv:2108.11221* (2021) [4] Sete, et al **arXiv:2402.04238** (2024)



min ΔSNAIL-Qubit (GHz) min Δ SNAIL-Qubit (GHz)

min ΔSNAIL-Qubit (GHz)

Separation required for high fidelity gates?



Interaction influence scales with number of neighboring modules